



Europäisches Patentamt  
European Patent Office  
Office européen des brevets



Publication number: **0 607 637 A2**

(12)

## EUROPEAN PATENT APPLICATION

(21) Application number: **93250361.8**

(51) Int. Cl.<sup>5</sup>: **A61N 1/39**

(22) Date of filing: **23.12.93**

(30) Priority: **07.01.93 US 1454**

(43) Date of publication of application:  
**27.07.94 Bulletin 94/30**

(84) Designated Contracting States:  
**BE DE ES FR GB IT NL**

(71) Applicant: **INCONTROL, INC.**  
**6675 185th Avenue, N.E.,**  
**Suite 100**  
**Redmond, WA 98052(US)**

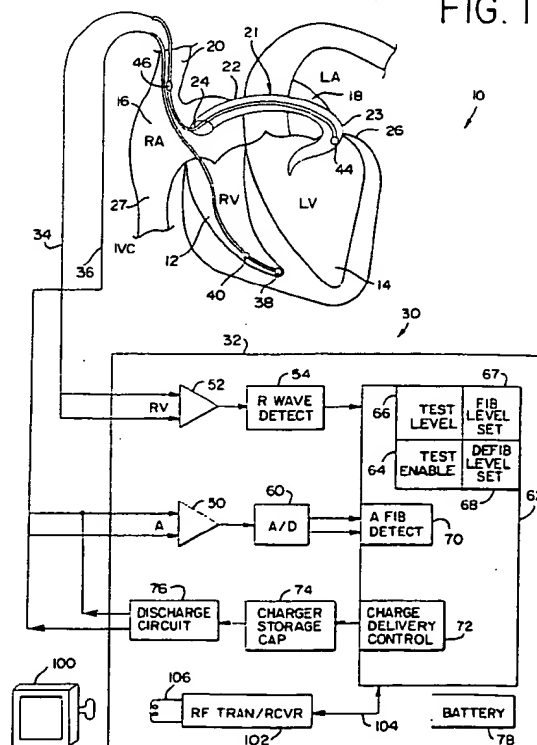
(72) Inventor: **Ayers, Gregory M.**  
**28308 NE 146th Street**  
**Duvall, Washington 98019(US)**

(74) Representative: **UEXKÜLL & STOLBERG**  
**Patentanwälte**  
**Beselerstrasse 4**  
**D-22607 Hamburg (DE)**

(54) **Atrial defibrillator and method for setting energy threshold values.**

(57) An implantable atrial defibrillator (30) cardioverts the atria (16, 18) of a patient's heart (10) and determines the quantity of cardioverting electrical energy required for cardioverting the atria of the patient. The atrial defibrillator includes a detector (50) for detecting atrial activity of the heart and an atrial fibrillation detector (70) for determining when the atria of the heart are in fibrillation. A delivery stage (76) is selectively operable in a test mode for applying fibrillation inducing electrical energy to the atria when the atria are not in fibrillation for inducing fibrillation. When fibrillation is induced, the delivery stage repeatedly applies test cardioversion electrical energy to the atria until the atria are cardioverted. Thereafter, a value indicative of the quantity of the test cardioversion energy last applied to the atria is stored in a store (68) for future reference when the atrial defibrillator is in a normal operating mode.

FIG. 1



## BACKGROUND OF THE INVENTION

The present invention generally relates to an atrial defibrillator for applying cardioverting electrical energy to the atria of a human heart in need of cardioversion. The present invention is more particularly directed to a fully automatic implantable atrial defibrillator which is selectively operable in a test mode for setting a threshold level for the cardioverting electrical energy. More specifically, the atrial defibrillator of the present invention, when the test mode is enabled by a physician, induces atrial fibrillation when the atria are not in fibrillation and thereafter repeatedly applies cardioverting electrical energy to the atria with incrementally increased quantities of electrical energy until the atria are successfully cardioverted. Thereafter, a value indicative of the last applied quantity of energy is stored in memory for reference during subsequent normal operation.

Atrial fibrillation is probably the most common cardiac arrhythmia. Although it is not usually a life threatening arrhythmia, it is associated with strokes thought to be caused by blood clots forming in areas of stagnant blood flow as a result of prolonged atrial fibrillation. In addition, patients afflicted with atrial fibrillation generally experience palpitations of the heart and may even experience dizziness or even loss of consciousness.

Implantable atrial defibrillators have been proposed to provide patients suffering from occurrences of atrial fibrillation with relief. Unfortunately, to the detriment of such patients, none of these atrial defibrillators have become a commercial reality. Two such proposed defibrillators, although represented as being implantable, were not fully automatic, requiring human interaction for cardioverting or defibrillating the heart. Both of these proposed defibrillators require the patient to recognize the symptoms of atrial fibrillation with one defibrillator requiring a visit to a physician to activate the defibrillator and the other defibrillator requiring the patient to activate the defibrillator with an external magnet.

Improved atrial defibrillators and lead systems which exhibit both automatic operation and improved safety include an atrial fibrillation detector which automatically detects when the atria are in fibrillation and in need of cardioversion. A cardioverter stage is responsive to the atrial fibrillation detector detecting atria fibrillation and automatically delivers cardioverting electrical energy to the atria.

Once an atrial defibrillator is implanted, it is desirable, at periodic intervals, to set the quantity of cardioverting electrical energy delivered to the atria at an energy level which provides a high probability of successful cardioversion but which is not excessively large. This is due to the fact that

the electrical energy to cardiovert the atria may be painful to the patient or at least cause discomfort. Hence, it is most desirable to set the energy level to just above a threshold level which will provide the high probability of successful cardioversion. Since the threshold level will be different for each patient and changes over time, it is necessary for the physician to induce atrial fibrillation in a patient and then perform a series of cardioverting electrical energy applications to the atria at incrementally increasing energy levels until the threshold level is determined by the successful cardioversion of the atria. The series of applications is preferably started at a low energy believed to be insufficient to cardiovert the atria of the patient.

One method of inducing atrial fibrillation in a patient is to apply rapid pacing pulses to the atria or apply a series of premature pacing pulses to the atria of the patient. This approach however has severe ramifications.

Firstly, if the rapid pacing pulses are provided by the atrial defibrillator through the atrial defibrillator implanted lead system, the energy required to induce atrial fibrillation may cause excessive drain on the depletable power source, such as a battery, which supplies power to the implanted defibrillator. This excessive drain can prematurely deplete the atrial defibrillator battery and require more frequent replacement of the atrial defibrillator than desired. If the pacing pulses are not applied by the implanted defibrillator, but by an external pacing source, an additional catheter lead is required necessitating an invasive procedure to at least temporarily implant the additional catheter. This not only causes inconvenience and discomfort to the patient but additionally imposes mobility restraints of more than four hours on the patient and the concomitant mortality associated with such a procedure. Furthermore, with such rapid atrial pacing, there is always the probability of inducing undesirable other arrhythmias of the heart.

The atrial defibrillator of the present invention overcomes the disadvantages expressed above with respect to the above described method of inducing atrial fibrillation for the purpose of permitting the determination of a patient's atrial defibrillation energy threshold level. The atrial defibrillator of the present invention is selectively operable, by a physician, into a test mode wherein, in the absence of natural atrial fibrillation, electrical energy is applied to the atria having a quantity less than the quantity required for atrial defibrillation or cardioversion and at a time during the cardiac cycle of the heart when the heart is most vulnerable to the inducement of atrial fibrillation. More particularly, the electrical energy is applied in timed relation to a sensed electrical activation (R wave) of the heart and preferably in synchrony with a sensed R wave

which, in most patients, occurs in time closely to the relative refractory of the atria. The atrial fibrillation inducing electrical energy is thus applied at a time during the cardiac cycle of the heart when the heart is most vulnerable to induced atrial fibrillation. In general, only one such application of electrical energy will be required for inducing atrial fibrillation. However, if more than one such application is required, the atrial defibrillator provides additional atrial fibrillation inducing electrical energy applications to the atria until atrial fibrillation is induced. Even if more than one such application is required, since the energy is applied when the heart is most vulnerable to induced atrial fibrillation, only a limited number of such applications will be required. This reduces the amount of total electrical energy required to induce atrial fibrillation as compared to the total energy required with rapid atrial pacing to accomplish this purpose. Hence, the atrial fibrillation is induced by the atrial defibrillator and its implanted lead system to thus negate the need for an invasive procedure for the temporary implantation of an additional catheter. Also, since the energy is applied in synchrony with an R wave, the inducement of ventricular fibrillation or other undesirable arrhythmias is avoided.

As will be seen hereinafter, and in accordance with the present invention, when atrial fibrillation is successfully induced, the atrial defibrillator, while still in the test mode, provides a series of cardioverting electrical energy applications to the atria. The cardioverting applications begin at an energy level which is lower than that expected for successful cardioversion and the energy level is incrementally increased for each application. When cardioversion of the atria is successful, a value corresponding to the last quantity of cardioverting electrical energy applied to the atria is stored in a memory for later reference in treating natural atrial fibrillation and the atrial defibrillator exits the test mode and reenters its normal mode of operation for cardioverting the atria when the atria are in need of cardioversion.

### **SUMMARY OF THE INVENTION**

The invention therefore provides an implantable atrial defibrillator adapted to be implanted beneath the skin of a patient for cardioverting the atria of a patient's heart when the atria are in need of cardioversion. The atrial defibrillator includes a first sensor for sensing atrial activity of the heart and an atrial fibrillation detector responsive to the first sensor for determining when the atria of the heart are in fibrillation and in need of cardioversion and when the atria of the heart are not in fibrillation. The atrial defibrillator further includes an applying stage for applying electrical energy to the atria of the pa-

tient's heart wherein the applying stage is selectively operable and responsive to the atrial fibrillation detector for applying first electrical energy to the atria for inducing fibrillation of the atria of the patient's heart when the atria of the heart are not in fibrillation and selectively operable and responsive to the atrial fibrillation detector for applying second electrical energy to the atria for cardioverting the atria of the patient's heart when the atria are in fibrillation and in need of cardioversion.

### **BRIEF DESCRIPTION OF THE DRAWINGS**

Figure 1 is a schematic block diagram of a fully implantable atrial defibrillator embodying the present invention for applying defibrillating electrical energy to the atria of a human heart and which is shown in association with a human heart in need of atrial fibrillation monitoring and potential cardioversion of the atria.

Figure 2 is a flow diagram illustrating the manner in which the atrial defibrillator of Figure 1 may be implemented in accordance with the present invention for inducing atrial fibrillation of the heart and thereafter applying defibrillating or cardioverting electrical energy to the atria of the heart to determine the atrial cardioversion threshold level of the heart.

### **DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT**

Referring now to Figure 1, it illustrates a fully implantable atrial defibrillator 30 embodying the present invention shown in association with a schematically illustrated human heart 10 in need of atrial fibrillation monitoring and potential cardioversion of the atria. The portions of the heart 10 illustrated in Figure 1 are the right ventricle 12, the left ventricle 14, the right atrium 16, the left atrium 18, the superior vena cava 20, the coronary sinus channel 21 which, as used herein, denotes the coronary sinus 22 and the great cardiac vein 23, the coronary sinus ostium or opening 24, the left ventricular free wall 26 and the inferior vena cava 27. In addition, as used herein, the term "electrical activations" denotes R waves of the heart cardiac cycle which are depolarizations of the ventricles 12 and 14.

The atrial defibrillator 30 generally includes an enclosure 32 for hermetically sealing the internal circuit elements of the atrial defibrillator to be described hereinafter, an endocardial first lead 34, and an intravascular second lead 36. The enclosure 32 and first and second leads 34 and 36 are arranged to be implanted beneath the skin of a patient so as to render the atrial defibrillator 30 fully implantable.

The endocardial first lead 34 preferably comprises a endocardial bi-polar lead having electrodes 38 and 40 arranged for establishing electrical contact with the right ventricle 12 of the heart 10. The electrodes 38 and 40 permit bi-polar sensing of ventricular activations in the right ventricle. As illustrated, the lead 34 is preferably fed through the superior vena cava 20, into the right atrium 16, and then into the right ventricle 12 as illustrated.

The second lead 36 generally includes a first or tip electrode 44 and a second or proximal electrode 46. As illustrated, the second lead 36 is flexible and arranged to be passed down the superior vena cava 20, into the right atrium 16, into the coronary sinus ostium 24, and advanced into the coronary sinus channel 21 of the heart near the left side thereof so that the first or tip electrode 44 is within the coronary sinus channel 21 either within the coronary sinus 22 adjacent the left ventricle 14 and beneath the left atrium 18 or most preferably within the great cardiac vein 23 adjacent the left ventricle 14 and beneath the left atrium 18. The electrodes 44 and 46 are spaced apart such that when the first electrode 44 is positioned as described above, the second electrode 46 is in the right atrium 16. The first electrode 44 together with the second electrode 46 provide bi-polar sensing of heart activity in the atria 16 and 18. The first electrode 44 and the second electrode 46 further provide for the delivery of defibrillating electrical energy to the atria. Because the first electrode 44 is located beneath the left atrium 18 near the left ventricle 14 and the second electrode 46 is within the right atrium 16, the electrical energy applied between these electrodes will be substantially confined to the atria 16 and 18 of the heart 10. As a result, the electrical energy applied to the right ventricle 12 and left ventricle 14 when the atria are cardioverted or defibrillated will be minimized. This greatly reduces the potential for ventricular fibrillation of the heart to be induced as a result of the application of defibrillating electrical energy of the atria of the heart. The intravascular second lead 36, and more particularly the first electrode 44 and the second electrode 46 further provide for the delivery of atrial fibrillation inducing electrical energy to the atria in a manner to be described hereinafter.

Within the enclosure 32, the atrial defibrillator 30 includes a first sense amplifier 50, a second sense amplifier 52, and an R wave detector 54. The first sense amplifier 50 forms a first sensing means which, together with the first electrode 44 and second electrode 46 of the second lead 36 to which it is coupled detects atrial activity of the heart. The second sense amplifier 52 and the R wave detector 54 form a second sensing means which together with the first lead 34 to which sense amplifier 52 is coupled, senses ventricular activa-

tions of the right ventricle 12.

The output of the first sense amplifier 50 is coupled to an analog to digital converter 60 which converts the analog signal representative of the sensed atrial activity of the heart to digital samples for further processing in a manner to be described hereinafter. The output of the second sense amplifier 52 is coupled to the R wave detector 54. The R wave detector 54 is of the type well known in the art which provides an output pulse upon the occurrence of an R wave being sensed during a cardiac cycle of the heart.

The enclosure 32 of the atrial defibrillator 30 further includes a microprocessor 62. The microprocessor 62 is preferably implemented in a manner as disclosed in the aforementioned copending U.S. Applications, Serial Numbers 07/685,130 and 07/856,514 and further as described hereinafter with respect to the flow diagram of Figure 2. The implementation of the microprocessor 62 in accordance with this embodiment of the present invention results in a plurality of functional stages. The stages include a test enable 64, a test level set stage 66, a fibrillation level set stage 67, a defibrillation level set or storage stage 68, an atrial arrhythmia detector in the form of an atrial fibrillation detector 70, and a charge delivery and energy control stage 72.

The microprocessor 62 is arranged to operate in conjunction with a memory (not shown) which may be coupled to the microprocessor 62 by a multiple-bit address bus (not shown) and a bi-directional multiple-bit databus (not shown). This permits the microprocessor 62 to address desired memory locations within the memory for executing write or read operations. During a write operation, the microprocessor stores data, such as time intervals or operating parameters in the memory at the addresses defined by multiple-bit addresses conveyed over the address bus and conveys the data to the memory 92 over the multiple-bit data bus. During a read operation, the microprocessor 62 obtains data from the memory at the storage locations identified by the multiple-bit addresses provided over the address bus and receives the data from the memory over the bi-directional data bus.

For entering operating parameters into the microprocessor 62, such as fibrillation energy levels into stage 67 or defibrillation energy levels into stage 68, or for receiving operating commands such as a test enable command, the microprocessor 62 receives the programmable operating parameters and operating commands from an external controller 100 which is external to the skin of the patient and under the control of an operator, such as a physician. The external controller 100 is arranged to communicate with a receiver/transmitter 102 which is coupled to the micro-

processor 62 over a bi-directional bus 104. The receiver/transmitter 102 may be of the type well known in the art for conveying various information which it obtains from the microprocessor 62 to the external controller 100 or for receiving programming parameters and operating commands from the external controller 100 which the receiver/transmitter 102 then conveys to the microprocessor 62 for storage in interval memory, such as stages 67 and 68, or in the aforementioned external memory within enclosure 32.

The receiver/transmitter 102 includes a transmitting coil 106 so that the receiver/transmitter 102 and coil 106 form a communication means. Such communication means are well known in the art and may be utilized as noted above for receiving commands from external to the implantable enclosures 32 and for transmitting data to the external controller 100 from the implanted enclosure 32. One such communication system is disclosed, for example, in U.S. Patent No. 4,586,508.

To complete the identification of the various structural elements within the enclosure 32, the atrial defibrillator 30 further includes a charger and storage capacitor circuit 74 of the type well known in the art which charges a storage capacitor to a predetermined voltage level and a discharge circuit 76 for discharging the storage capacitor within circuit 74 by a predetermined amount to provide a controlled discharge output of electrical energy when required to the atria of the heart. To that end, the discharge circuit 76 is coupled to the first electrode 44 and the second electrode 46 of the second lead 36 for applying the cardioverting or defibrillating electrical energy to the atria and for applying atrial fibrillation inducing electrical energy to the atria. Lastly, the defibrillator 30 includes a depletable power source 78, such a lithium battery, for providing power to the electrical components of the atrial defibrillator 30.

When the atrial defibrillator 30 is operative in its normal operating mode, the sense amplifier 52 and the R wave detector 54 continuously detect the occurrence of electrical activations of the right ventricle 12. As disclosed in the aforementioned copending U.S. Applications Serial Numbers 07/685,130 and 07/856,514, herein incorporated by reference, when the time intervals between immediately successive R waves indicate the probability of an episode of atrial fibrillation, the microprocessor 62 enables the atrial fibrillation detector 70, sense amplifier 50, and the analog to digital converter 60. If the atrial fibrillation detector 70 determines that the atria 16 and 18 are in fibrillation and thus in need of cardioversion, the charge delivery control 72 causes the charger and storage capacitor circuit 74 to charge the storage capacitor within circuit 74 to a level set in the defibrillator level set

stage 68. Then, when an electrical activation (R wave) is sensed by sense amplifier 52 and R wave detector 54, the charge delivery control 72 cause the discharge circuit 76 to discharge the capacitor in the charge storage circuit 74 into electrodes 44 and 46 for cardioverting the atria. The foregoing is repeated until the atria are successfully cardioverted.

In accordance with the present invention, the atrial defibrillator 30 is also selectively operable in a test mode for determining the defibrillation energy threshold level of the patient for providing the defibrillation level set stage 68 with a value for reference by the atrial defibrillator in its normal operation so that the defibrillating electrical energy applied to the atria is of a quantity which provides a high probability of successful cardioversion but which is not excessively large. The implementation of the atrial defibrillator 30 to that end is illustrated in Figure 2.

Referring now to Figure 2, it illustrates, in flow diagram format, the implementation of the atrial defibrillator 30 for both the normal operating mode and the test operating mode. The normal operating mode is implemented in accordance with a normal operating mode section 110 and the test mode is implemented in accordance with a test mode section 112.

Both the normal operating mode and the test operating mode require interrogation of the atrial fibrillation detector 70 in accordance with step 114. If atrial fibrillation is detected in step 114, the capacitor within the charger storage circuit 74 is charged in step 116 to the level set in the defibrillation level set stage 68. Thereafter, in step 118, it is determined if an electrical activation (R wave) is detected by sense amplifier 52 and R wave detector 54. When an R wave is detected in step 118, the microprocessor then causes the discharge circuit 76 to discharge the storage capacitor of circuit 74 in accordance with step 120. The energy discharged from the storage capacitor is delivered to the electrodes 44 and 46 for application across the atria 16 and 18 for cardioverting the atria. After the capacitor has been discharged, the microprocessor returns to once again interrogate the atrial fibrillation detector. Upon repeating step 114, if atrial fibrillation is still present, steps 116, 118, and 120 are then repeated. If the atria are successfully cardioverted, the atrial defibrillator 30 returns to normal sensing.

When it is desired to enter the test mode, the physician transmits from the external controller 100 a test enable command to the transmitter/receiver 102 of the atrial defibrillator 30. The test enable command is conveyed to the test enable stage 64 of microprocessor 62 to set the test enable stage 64.

If it is determined in step 114 that the atria are not in fibrillation, the microprocessor then determines in step 122 if the test enable stage 64 has been set. If the test enable stage has not been set, the microprocessor returns to start. If the test enable stage has been set, the charge delivery control 72 then causes the capacitor within the charger storage circuit 74 to be charged to a level previously entered into the fibrillation level set stage 67. The capacitor is thus charged in accordance with step 124. The fibrillation level is preferably a quantity of energy which is less than the quantity of energy for defibrillating the atria. For example, the electrical energy for defibrillating the energy may be in a range of .5 to 3.0 joules and preferably between .5 and 1 joule. In contrast, the quantity of energy for inducing fibrillation may be in a range of .05 to .4 joules and preferably .1 joule.

Following step 124, the atrial defibrillator 30 determines in step 126 if an R wave has been detected. When an R wave is detected in step 126, the atrial defibrillator immediately thereafter in step 128 discharges the capacitor of the charger storage circuit 74 across the atria by applying the discharged energy to electrodes 44 and 46 of the second lead 36. As previously mentioned, the fibrillation inducing energy is preferably applied to the atria in synchronism with an R wave because, in most patients, a sensed R wave will occur in time in substantial correspondence to the T wave of the atria and hence, at a time when the atria are most vulnerable to induced fibrillation. Once the fibrillation inducing electrical energy is applied to the atria, the atrial fibrillation detector 70 is once again interrogated in step 130. If the atria are not in fibrillation, the atrial defibrillator then returns to repeat steps 124, 126, 128, and 130. As previously mentioned, in most cases, a single application of fibrillation inducing electrical energy will only be required to induce atrial fibrillation.

Once atrial fibrillation is induced, the atrial defibrillator then proceeds to step 132 for charging the capacitor in the charger storage circuit 74 to an initial test level in accordance with an initial test level value provided by the test level stage 66. The initial test level is preferably a level which is not expected to provide successful cardioversion of the atria. This assures that the determination of the patient's threshold level begins at a level below the expected threshold level.

Following step 132, the atrial defibrillator then in step 134 determines when an R wave is sensed by sense amplifier 52 and R wave detector 54. When an R wave is sensed, the capacitor in circuit 74 is discharged by the discharge circuit 76 in step 136.

Following step 136, the atrial fibrillation detector 70 is interrogated in step 138 to determine if

the application of the test cardioversion energy is successful in cardioverting the atria. If the atrial fibrillation detector 70 indicates that the atria are still in fibrillation, the defibrillator then in step 140 incrementally increases the test level by, for example, .2 joules, and returns to step 132 for repeating step 132, 134, 136, and 138. This results in repeated applications of cardioverting electrical energy to the atria with each application being incrementally increased over the immediately preceding application.

When it is determined in step 138 that the atria have been successfully cardioverted, the atrial defibrillator then in step 142 enters into the defibrillation level set stage 68 a value corresponding to the last test level utilized which was successful in cardioverting the atria. Also in step 142, the test enable stage 64 may be reset for exiting the test mode at which point the atrial defibrillator reenters the normal operating mode. The defibrillation level set stage 68 will then contain a value corresponding to the last test level utilized during the test mode and hence the proper threshold level for the individual patient.

While a particular embodiment of the present invention has been shown and described, modifications may be made, and it is therefore intended in the appended claims to cover all such changes and modifications which fall within the true spirit and scope of the invention.

## Claims

1. An implantable atrial defibrillator (30) adapted to be implanted beneath the skin of a patient for cardioverting the atria (16, 18) of a patient's heart (10) when the atria are in need of cardioversion, the atrial defibrillator characterized by:

a first sensor 52 for sensing atrial activity of the heart;

an atrial fibrillation detector (70) responsive to said first sensor for determining when the atria of the heart are in fibrillation and in need of cardioversion and when the atria of the heart are not in fibrillation; and

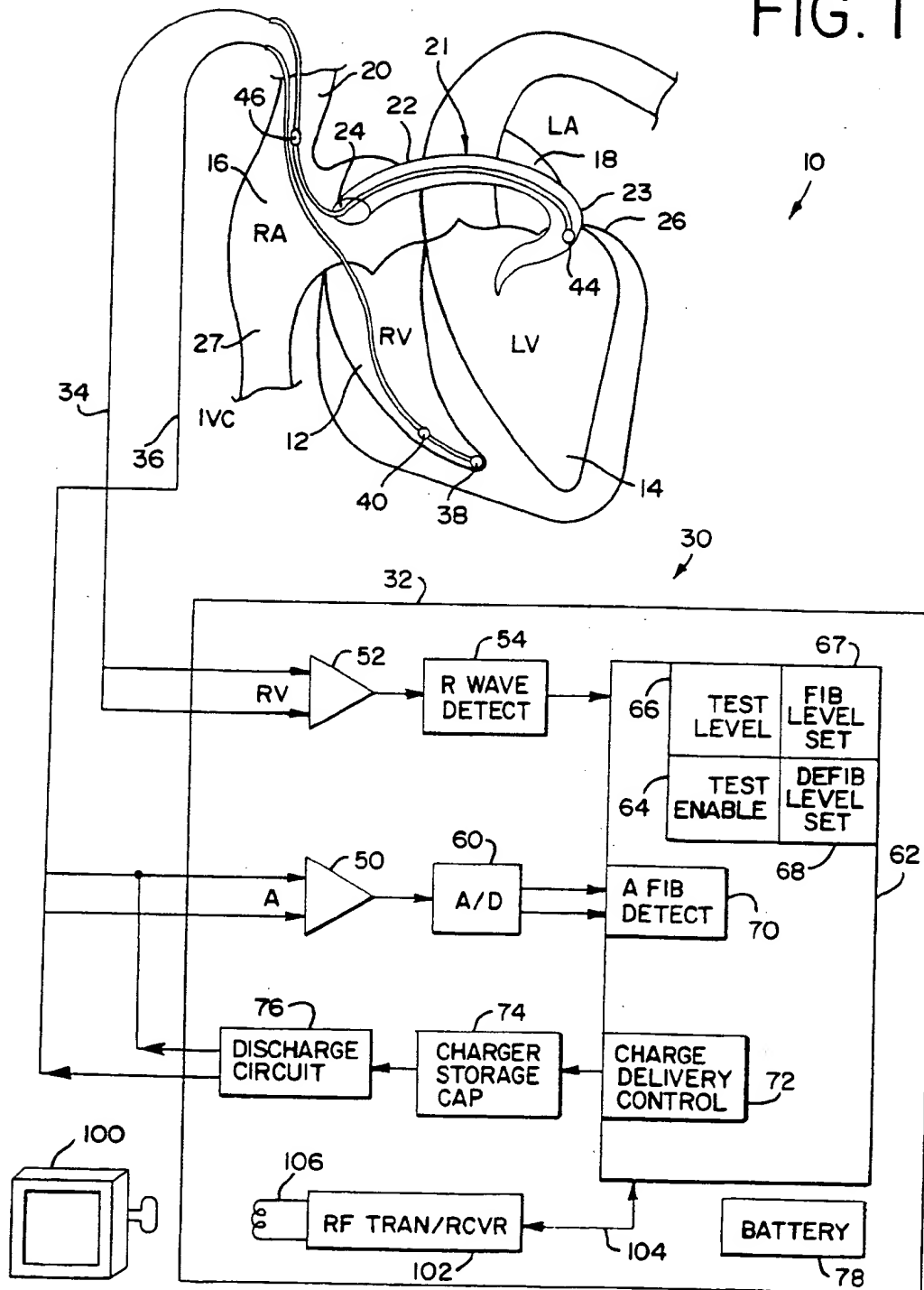
an applying stage (76) for applying electrical energy to the atria of the patient's heart, said applying stage being selectively operable and responsive to said atrial fibrillation detector for applying first electrical energy to the atria for inducing fibrillation of the atria of the patient's heart when the atria of the heart are not in fibrillation and selectively operable and responsive to said atrial fibrillation detector for applying second electrical energy to the atria for cardioverting the atria of the patient's heart when the atria are in fibrillation and in need of

cardioversion.

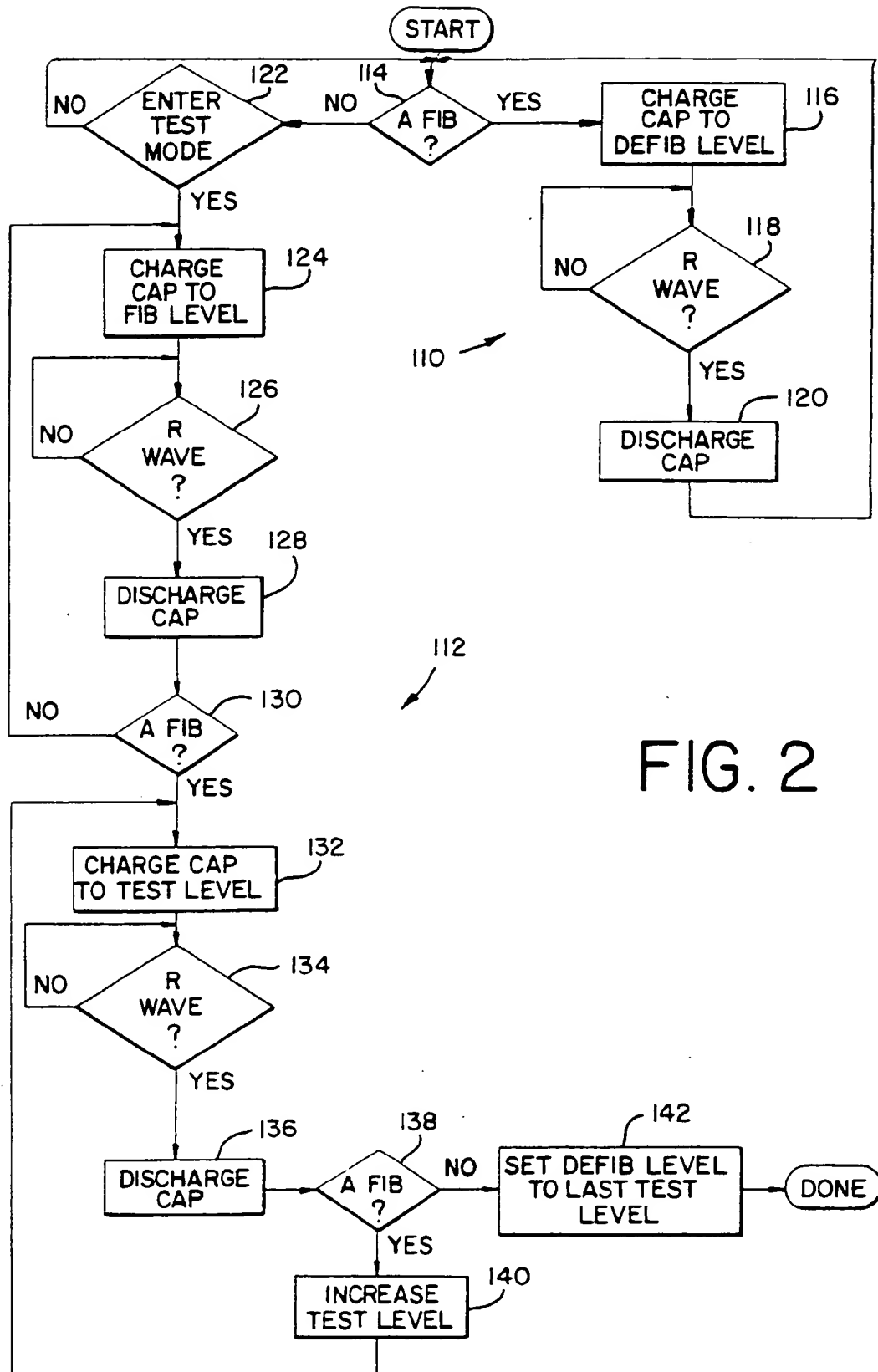
energy to atria in synchrony with a sensed electrical activation of the heart.

2. The atrial defibrillator of claim 1 further characterized by the applying stage applying the first electrical energy during a predetermined portion of a cardiac cycle of the heart. 5
  
3. The atrial defibrillator of claim 2 further characterized by a second sensor (54) for sensing electrical activations of the heart and wherein the applying stage is responsive to the second sensor for applying the first electrical energy during an electrical activation of the heart. 10
  
4. The atrial defibrillator of claims 1, 2, or 3 wherein the first electrical energy is of lesser quantity than the second electrical energy. 15
  
5. The atrial defibrillator of claims 1, 2, 3, or 4 further characterized by the applying stage applying the second electrical energy having an initial quantity of energy and thereafter repeatedly applying the second electrical energy having incrementally increased quantities of energy until the atria are cardioverted. 20 25
  
6. The atrial defibrillator of claim 5 further characterized by the atrial fibrillation detector determining if the atria are in fibrillation after each repeated application of the second electrical energy and the applying stage being responsive to the atrial fibrillation detector for terminating the repeated application of the second electrical energy. 30 35
  
7. The atrial defibrillator of claim 6 further characterized by a store (68) for storing a value indicative of the last quantity of the second electrical energy applied to the atria of the heart. 40
  
8. The atrial defibrillator of claims 1, 2, 3, 4, 5, 6, or 7 further characterized by enable stage (64) for selectively enabling the applying stage to apply the first electrical energy to the atria. 45
  
9. The atrial defibrillator of claims 1, 2, 3, 4, 5, 6, 7, or 8 further characterized by the applying stage including a lead (36) for applying the first and second electrical energies to the atria of the heart. 50
  
10. The atrial defibrillator of claim 3 further characterized by a receiver (102) for receiving an enable command from external to the skin of the patient, and the applying stage being responsive to the enable command and the second sensor for delivering the first electrical 55

FIG. 1











Europäisches Patentamt  
European Patent Office  
Office européen des brevets



Publication number: **0 607 637 A3**

(12)

## EUROPEAN PATENT APPLICATION

(21) Application number: **93250361.8**

(51) Int. Cl.<sup>5</sup>: **A61N 1/39**

(22) Date of filing: **23.12.93**

(30) Priority: **07.01.93 US 1454**

(43) Date of publication of application:  
**27.07.94 Bulletin 94/30**

(84) Designated Contracting States:  
**BE DE ES FR GB IT NL**

(88) Date of deferred publication of the search report:  
**07.12.94 Bulletin 94/49**

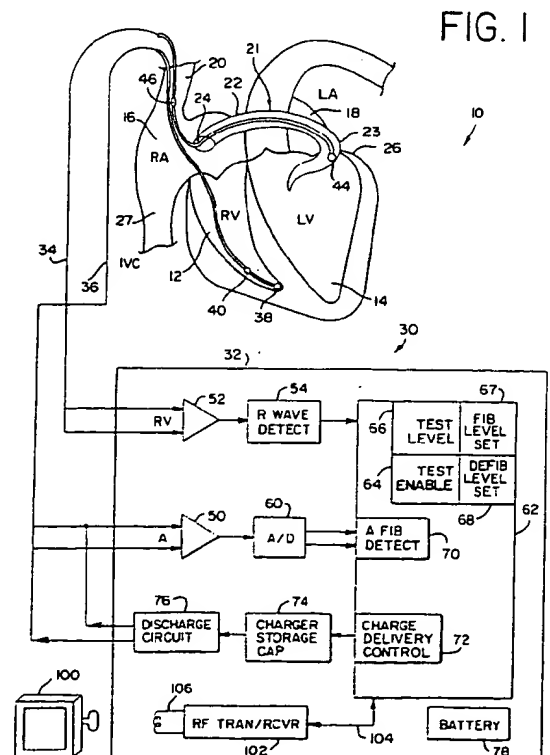
(71) Applicant: **INCONTROL, INC.**  
**6675 185th Avenue, N.E.,**  
**Suite 100**  
**Redmond, WA 98052 (US)**

(72) Inventor: **Ayers, Gregory M.**  
**28308 NE 146th Street**  
**Duvall, Washington 98019 (US)**

(74) Representative: **UEXKÜLL & STOLBERG**  
**Patentanwälte**  
**Beselerstrasse 4**  
**D-22607 Hamburg (DE)**

(54) **Atrial defibrillator and method for setting energy threshold values.**

(57) An implantable atrial defibrillator (30) cardioverts the atria (16, 18) of a patient's heart (10) and determines the quantity of cardioverting electrical energy required for cardioverting the atria of the patient. The atrial defibrillator includes a detector (50) for detecting atrial activity of the heart and an atrial fibrillation detector (70) for determining when the atria of the heart are in fibrillation. A delivery stage (76) is selectively operable in a test mode for applying fibrillation inducing electrical energy to the atria when the atria are not in fibrillation for inducing fibrillation. When fibrillation is induced, the delivery stage repeatedly applies test cardioversion electrical energy to the atria until the atria are cardioverted. Thereafter, a value indicative of the quantity of the test cardioversion energy last applied to the atria is stored in a store (68) for future reference when the atrial defibrillator is in a normal operating mode.





European Patent  
Office

## EUROPEAN SEARCH REPORT

Application Number  
EP 93 25 0361

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int.Cl.5)
Y	WO-A-92 18198 (INCONTROL) * page 4, line 31 - page 9, line 5; figures *	1-4	A61N1/39
A	---	8,10	
Y	US-A-5 105 809 (BACH, JR. ET AL.) * column 2, line 32 - column 4, line 9; figures *	1-7	
A	---	8,9	
Y	US-A-4 787 389 (TARJAN) * column 3, line 41 - column 5, line 55; figures *	5-7	
A	---	1,4,9	
A	EP-A-0 467 652 (TELECTRONICS) * page 4, column 4, line 1 - page 6, column 8, line 53; figures *	1-3,8,9	
A	US-A-5 107 834 (IDEKER ET AL.) * column 3, line 48 - column 6, line 16; figures *	1,3,4,6	TECHNICAL FIELDS SEARCHED (Int.Cl.5)
A	EP-A-0 410 954 (COHEN)  * page 10, column 16, line 41 - page 11, column 17, line 45 * * page 15, column 26, line 35 - page 16, column 27, line 20 * * page 17, column 29, line 11 - line 34; figures *  -----	1-3,5,8, 9	A61N
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 28 September 1994	Examiner Rakotondrajaona, C
<b>CATEGORY OF CITED DOCUMENTS</b> X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document  T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application I : document cited for other reasons ----- & : member of the same patent family, corresponding document			